

DOCKET NO. 31-5-3

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Young-Kai Chen, et al.

Serial No.: 10/771,089

Filed: February 3, 2004

For: OPTICAL DIGITAL TO ANALOG CONVERTER

Group No.: 2819

Examiner: Howard L. Williams

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner of Patents and Trademarks, Washington, D.C. 20231, on 10-15-2005 (Date)  
Elizabeth Schumacher  
(Printed or typed name of person signing the certificate)  
Elizabeth Schumacher  
(Signature of the person signing the certificate)

Sir:

FOREIGN SEARCH INFORMATION DISCLOSURE STATEMENT

In accordance with 37 C.F.R. §1.56 and the provisions of 37 C.F.R. §§1.97 and 1.98 and §609 of the Manual of Patent Examining Procedure, Applicant hereby makes a disclosure of the patents, publications and other information listed below and on the accompanying form IDS by Applicant, which may be potentially material to the patentability of the invention disclosed in the above-referenced application. Pursuant to § 1.97(e) the Applicant hereby states that each item of information contained in the information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application.

**Other References:**

Yacoubian, et al. "Digital-To-Analog Conversion Using Electrooptic Modulators", IEEE Photonics Technology Letters, IEEE Inc., New York, Vol., 15, No. 1, January 2003; pages 117-119.

Leven, et al., "Microwave Photonic Vector 1-10 Modulator", Leos 2003, 16th Annual Meeting of the IEEE Lasers & Electro-Optics Society, Tucson, Arizona, October 27-28, 2003; and Electro-Optics Society, New York, New York, Vol., Vol. 1 of 2, October 26, 2003; pages 935-936.

Saida, et al., "Integrated Optical Digital-To-Analogue Converter and Its Application to Pulse Pattern Recognition", Electronics Letters, IEE Stevenage, GB, Vol. 37, No. 20, September 27, 2001, pages 1237-1238.

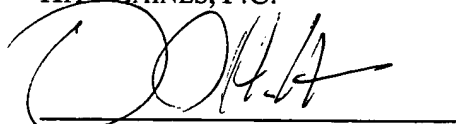
Applicant hereby certifies that each item of information contained in the Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application.

As attorney for the Applicant, I am signing below on the basis of the information supplied by an individual designated in § 1.56(c).

The Commissioner is hereby authorized to charge any fees or credit any overpayments to Deposit Account No. 08-2395.

Respectfully submitted,

HITT GAINES, P.C.

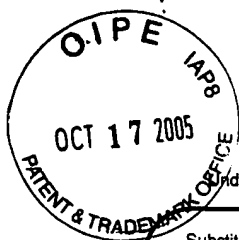


David H. Hitt

Registration No. 33,182

Date: OCTOBER 12/2005

Hitt Gaines, P.C.  
P.O. Box 832570  
Richardson, Texas 75083-2570  
(972) 480-8800



PTO/SB/08b (07-05)  
Approved for use through 06/30/2006. OMB 0651-0031  
U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number.

Substitute for form 1449B/PTO

## INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Sheet

1

of

1

### Complete if Known

Application Number	10/771,089
Filing Date	February 3, 2004
First Named Inventor	Young-Kai Chen
Art Unit	2819
Examiner Name	Howard L. Williams
Attorney Docket Number	31-5-3

### NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
		Yacoubian, et al. "Digital-To-Analog Conversion Using Electrooptic Modulators", IEEE Photonics Technology Letters, IEEE Inc., New York, Vol., 15, No. 1, January 2003; pages 117-119.	
		Leven, et al., "Microwave Photonic Vector 1-10 Modulator", Leos 2003, 16th Annual Meeting of the IEEE Lasers & Electro-Optics Society, Tucson, Arizona, October 27-28, 2003; and Electro-Optics Society, New York, New York. Vol. Vol. 1 of 2. October 26, 2003; pages 935-936.	
		Saida, et al., "Integrated Optical Digital-To-Analogue Converter and Its Application to Pulse Pattern Recognition", Electronics Letters, IEE Stevenage, GB, Vol. 37, No. 20, September 27, 2001, pages 1237-1238.	

Examiner  
Signature

Date  
Considered

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

<sup>1</sup> Applicant's unique citation designation number (optional). <sup>2</sup> Applicant is to place a check mark here if English language Translation is attached. This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

If you need assistance in completing the form, call 1-800-PTO-9199 and select option 2.